**MICHAEL HARRINGTON**

**11941 Spruce Canyon Circle, Golden Colorado, 80403**

**Email: harrymike2@msn.com**

**(303) 642-1123 (home) or (303) 908-6253 (Cell)**

**CAREER SUMMARY:**

Printed Circuit Board Designer/Engineer (was Sr. Engineering Tech) with Thirty years of experience of with, PCBA engineering and design, prototype builds, SMT rework/assembly, component level troubleshooting. I am an **expert** **user** of **Altium Designer** and **OrCAD PCB Layout**, **OrCAD** **Schematic Capture and Agile CM** tools.

**EXPERIENCE:**

**Harrymike Engineering Services,** Golden (Coal Creek Canyon), CO – **PCB Designer, Engineer & Owner** - **Present**

• Performing PCB Layout, Schematic Capture, Component Engineering and Altium Designer Training services.

• Supporting companies such as; Creation Technologies, Nine Dot Connects (VAR) and Sierra Nevada Corp.

**Aircell LLC,** Broomfield, CO – **PCB Designer and Engineer**  **2006 – December, 2012**

• Involved with designing, schematic capture and PCB layout for aviation communication products.

• Performed Component Engineering for Aircell and worked closely with Agile CM administrator.

**Starsys Research Inc. (Now SNC),** Boulder, CO– **PCB Designer and Sr. Engineering Technician** **2004 – 2005**

• Involved with designing, schematic capture and PCB layout for many aerospace products.

• Performed many Component Engineering and Sr. Engineering Technician functions.

Contract positions at **ITT Aerospace, Medivance, Aztek Engineering** and **Droplet Measurement Tech 2001 - 2006** • Performing PCB Design and many other Sr. Engineering Technician functions.

**Colorado MicroDisplay/Zight**,Boulder, CO – **PCB Designer and Sr. Engineering Technician** **1997 – 2001**

• Performed rigid and flexible PCB layout and schematic capture using Orcad Layout Plus.

• Designed many boards that incorporate single ended and/or differential impedance control.

• Supported engineers and customer shipments by performing many different engineering tasks.

• Built, debugged, repaired, and brought up many prototype circuit cards for a mini LCD product.

• Designed and built an electronics engineering lab for debug use and SMT assembly.

**Cobe BCT and Cobe Renal Care**, Lakewood, CO – **Sr. Engineering Technician** **1995 - 1997**

• Performed Engineering Technician functions for a project developing an Aphaeresis machine.

• Performed Engineering Technician functions for a project developing a Kidney Dialysis machine.

**StorageTek**, Boulder, CO – **Sr. Engineering Technician** **1994 - 1995**

• Performed Engineering Technician functions for a project developing RAID disk drive systems.

**Intellistor Inc. (Fujitsu-FCPA)**, Longmont, CO – **Sr. Engineering Technician** **1989 - 1994**

• Supported an electronics development lab, for thirty engineers, and three managers.

• Oversaw PCB layout of many PCBA’s, also created and tracked their bill of materials.

• Designed some PCBAs such as power control interfaces, op-panels, and an RS232 interface.

• Built all of the development lab’s prototype debug test stations.

• Utilized debug monitors and diags, logic analyzers, and digital storage oscilloscopes.

**Data General Corp**., Fountain, CO – **Electronic Technician 4 and Test Equipment Build Tech** **1984 - 1989**

• Responsible for component level repair of magnetic data storage devices and controllers,

power supplies, microprocessor based communication PCBA’s, and memory PCBA’s.

• Designed, built, upgraded, and wrote test procedures for repair center test equipment.

**Micro Machine and Esten Machine**, Westboro, MA – **CNC Machinist/Tool and Die Maker** **1979 - 1983**

**EDUCATION:**

**University of Colorado (UCCS)**, Colorado Springs, CO **1987 - 1989**

**Central New England College**, Westboro, MA **1984 - 1985**

**GTE Sylvania Technical School**, Waltham, MA **1983 - 1984**

Graduate of both Computers and Telecommunications Electronics certificate programs.

**Advanced IPC Certified Interconnect PCB Designer (CID & CID+)**